

Windows SMM Security Mitigations Table

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Abstract

This specification contains details of an Advanced Configuration and Power Interface (ACPI) table that was created for use with Windows operating systems that support Windows virtualization-based security (VBS) features.

This information applies for the following operating systems:

- Windows Server Technical Preview 2016
- Windows 10, version 1607

For the latest information, see:

<http://www.microsoft.com/whdc/system/platform/virtual>

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WINDOWS SMM SECURITY MITIGATIONS TABLE (WSMT) ACPI TABLE SPECIFICATION LICENSE

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Introduction

Microsoft introduced virtualization-based security (VBS) in Windows 10. Virtualization-based security utilizes the Microsoft hypervisor and other system level software in conjunction with the platform's hardware virtualization extensions to create a secure environment in which various security-related applications and functions can run.

For example, VBS allows for a hypervisor based Secure Kernel to interpose on the operation of kernel mode (CPL0) components in order to uphold overall system integrity after boot. This is achieved by creating a separate operating system context, controlled by the hypervisor and enforced in hardware virtualization features, and using this separate "synthetic" privilege level for applying security policies and protecting critical data.

The advent of VBS changes the traditional trust boundaries and security model on which System Management Mode (SMM¹) may have made assumptions. Historically, in many implementations of system firmware, SMM has treated all kernel OS components running at CPL0 as trusted code. Specifically, the traditional operating system is considered to be in the same trust boundary as SMM.

With the introduction of VBS, this no longer holds true. In the security model expected with VBS, the platform firmware executing in SMM must be deemed trusted by VBS, and the traditional operating system components executing in the kernel in CPL0 are not. In fact, SMM must now treat kernel OS components in CPL0 as potentially hostile.

To properly protect the secure environment created by VBS from exploits mounted via SMM, a number of changes to SMM programming practices and assumptions must be introduced. Without these changes, the Windows OS may choose to degrade or disable specific security features or capabilities in order to ensure the integrity of platform security assets, and provide a robust and reliable platform on which to develop new end user scenarios. However, the OS must be able to determine what SMM security mitigations have been implemented on a specific platform. Since SMM is opaque to the OS, and since the OS must assume SMM is within the same trust domain as the OS itself, the OS must rely on SMM firmware to accurately self-report which of the Microsoft recommended security best practices it has implemented. To accomplish this, Microsoft has defined the Windows SMM Security Mitigations Table (WSMT).

This paper serves to describe the format of the WSMT table, wherein firmware communicates specific patterns of protections that it has enabled and enforcements that it has put in place, in acknowledgement of this new trust boundary.

Support for the WSMT is included in the following versions of Windows:

- Windows Server Technical Preview 2016
- Windows 10, version 1607

¹ For the purposes of this document, SMM and ARM TrustZone are synonymous.

This document describes the layout of the WSMT table, together with a more detailed description of the layout and contents of the WSMT Protections Flags field.

Windows SMM Security Mitigations Table

To use the WSMT, system firmware must create a static WSMT table in the ACPI namespace of the platform. Supported versions of Windows operating systems read the WSMT early during initialization, prior to start of the ACPI interpreter and subsequent evaluation of the `_OSI` method. The Protection Flags field indicates the presence of specific BIOS security mitigations in system firmware. Firmware setting any of the Protections Flags represents an attestation by the platform to OSPM that the corresponding firmware feature or coding practice has been implemented. OSPM may not be able to functionally validate that this is indeed the case, and must rely on system firmware to accurately represent its capabilities. Windows operating systems may elect to enable, disable, or de-feature certain security features based on the presence of these SMM Protections Flags.

Windows SMM Security Mitigations Table Format

Table 1 details the layout of the WSMT.

Table 1. Windows SMM Security Mitigations Table

Field	Byte length	Byte offset	Description
ACPI Standard Header			
Signature	4	0	Signature for the WSMT
Length	4	4	Length, in bytes, of the WSMT. Must be 40 for Revision 1.
Revision	1	8	1
Checksum	1	9	Entire table, which must sum to zero
OEMID	6	10	Original equipment manufacturer (OEM) identifier (ID)
OEM Table ID	8	16	Manufacturer model ID
OEM Revision	4	24	OEM revision for supplied OEM table ID
Creator ID	4	28	Vendor ID of the ASL compiler utility that created the table
Creator Revision	4	32	Revision of the ASL compiler utility that created the table
Protection Flags	4	36	Container of a bitmask of the system implemented WSMT protections. Bits in this field represent that certain protections/enforcements are enabled and active for firmware executing in SMM context after ExitBootServices(). See Table 2 for a detailed description of this field.

WSMT Protections Flags

Table 2 describes the bit definitions for the Protections Flags field of the WSMT.

Table 2. Protection Flags Field

Length	Bit offset	Description
1	0	FIXED_COMM_BUFFERS If set, expresses that for all synchronous SMM entries, SMM will validate that input and output buffers lie entirely within the expected fixed memory regions.
1	1	COMM_BUFFER_NESTED_PTR_PROTECTION If set, expresses that for all synchronous SMM entries, SMM will validate that input and output pointers embedded within the fixed communication buffer only refer to address ranges that lie entirely within the expected fixed memory regions.
1	2	SYSTEM_RESOURCE_PROTECTION Firmware setting this bit is an indication that it will not allow reconfiguration of system resources via non-architectural mechanisms.
	31:3	Reserved; must return 0 when read.

WSMT Protections Flags Details

FIXED_COMM_BUFFERS – Firmware setting this bit should refer to the SMM Communication ACPI Table defined in the UEFI 2.6 specification. Firmware should also consider all other possible data exchanges between SMM and non-SMM, including but not limited to EFI_SMM_COMMUNICATION_PROTOCOL, ACPI NVS in ASL code, general purpose registers as buffer pointers, etc.

COMM_BUFFER_NESTED_PTR_PROTECTION – Firmware setting this bit must also set the FIXED_COMM_BUFFERS bit.

SYSTEM_RESOURCE_PROTECTION – After ExitBootServices(), firmware setting this bit shall not allow any software to make changes to the locations of: IOMMU's, interrupt controllers, PCI Configuration Space, the Firmware ACPI Control Structure (FACS), or any registers reported through ACPI fixed tables (e.g. PMx Control registers, reset register, etc.). This also includes disallowing changes to RAM layout and ensuring that decodes to RAM and any system resources as described above take priority over software configurable registers. For example, if software configures a PCI Express BAR to overlay RAM, accesses by the CPU to the affected system physical addresses must decode to RAM.